

Figure 5-1

1) Which of the following logic expressions represents the logic diagram in Figure 5-1? A) $X=\overline{A B}+\bar{A} \bar{B}$ B) $X=A \bar{B}+\bar{A} B$ C) $X=\bar{A} \bar{B}+A B$
D) $X=A B+A B$

2) A correct logic expression for Figure 5-2 is $\qquad$ .
3) How many gates, including inverters, are required to implement the equation, $X=A+A B+\bar{A} B$, as it is written?
$\begin{array}{lll}\text { A) } 1 & \text { B) } 2 & \text { C) } 3\end{array}$
se it $\qquad$
D) 4
4) The NAND gate is referred to as a "universal" gate, because it
5) $\qquad$
$\qquad$


D) Output (D)
A) Output (A)
6) For the network shown below, the boolean expression for $X$ is $\qquad$ -
7) $\qquad$

8) The simplest Boolean expression for the Karnaugh map below is $\qquad$
$\qquad$

A) $A B \bar{C}+A B C+A \bar{B} C$
B) $X=A C+B$
C) $X=A \bar{B}$
nswer Key
