

<b>Abbreviation</b>	<b>Full form</b>
ACC	Accumulator (register)
AF	Advanced Format
AGP	Address Generation Unit
AGP	Accelerated Graphics Port
ALU	Arithmetic Logic Unit
APU	Accelerated Processing Unit
ASCII	American Standard Code for Information Interchange
ATA	AT attachment
AU	Addressing Unit
AVX	Advanced Vector eXtensions
BCD	Binary Coded Decimal
BIOS	Basic Input Output System
bit	binary digit
BIU	Bus Interface Unit
BMP	Basic Multilingual Plane
BP	Base Pointer
CA	Computer Architecture
CAM	Content-Addressable Memory
CD	compact disc
CF	Carry Flag
CHS	Cylinder-Sector-Head
CISC	Complex Instruction Set Computer
CPU	Central Processing Unit
CU	Control Unit
CS	Code Segment (register)
DDR	Double Data Rate
DI	Destination Index (register)
DIMM	Dual Inline Memory Module
DLP	Data Level Parallelism
DMA	Direct Memory Access
DMI	Direct Media Interface
DRAM	Dynamic Random Access Memory
DS	Data Segment (register)
DVD	Digital Versatile/Video Disc
DVI	Digital Visual Interface
EA	Effective Address
EDF	Earliest Deadline First
EEPROM	Electrically Erasable Programmable ROM
EPIC	Explicitly Parallel Instruction Computing
EPROM	Erasable Programmable Read Only Memory
ES	Extra Segment (register)
EU	Execution Unit
EX	EXecution
FAT	File Allocation Table
FCFS	First Come First Served
FLOPS	Floating Point Operation Per Second

FPGA	Field-Programmable Gate Array
FSB	Front Side Bus
GB	gigabyte
GHz	gigahertz
GPGPU	General Purpose Graphics Processing Unit
GPT	GUID Partition Table
GPU	Graphics Processing Unit
HDD	Hard Disc Drive
HDMI	High Definition Multimedia Interface
HPC	High Performance Computing
HT	Hyper-Threading
I/O	Input/output
ID	Instruction Decode
IF	Interrupt Flag (SR bit)
IF	Instruction Fetch
ILP	Instruction Level Parallelism
Inf	Infinity
IOP	Input-Output Processor
IP	Instruction Pointer
IPC	Inter-Process Communication
IR	Instruction Register
IRQ	InterRupt Request
ISA	Instruction Set Architecture
ISR	Interrupt Service Routine
kB	kilobyte
LBA	logical block addressing
LED	Light-Emitting Diode
LIFO	Last In First Out
LRU	Least Recently Used
LSB	Least Significant Byte/Bit
MAR	Memory Address Register
MB	megabyte
MBR	Master Boot Record
MDR	Memory Data Register
MHz	megahertz
MIC	Many Integrated Core
MIMD	Multiple Instruction Multiple Data
MIPS	Million Instruction Per Second
MISD	Multiple Instruction Single Data
MMIO	Memory-mapped Input-Output
MMU	Memory Management Unit
MMX	MultiMedia eXtension
μP	Microprocessor
MPI	Message Passing Interface
MSB	Most Significant Byte/Bit
MUX	MULTipeXer
NaN	Not a Number
NMI	Non-Maskable Interrupt

NUMA	Non-Uniform Memory Access
OF	Overflow Flag (SR bit)
OF	Operand Fetch
OoOE	Out-of-Order Execution
OS	Operating System
PC	Program Counter (register)
PC	Personal Computer
PCB	Process Control Block
PCI	Peripheral Component Interconnect
PCIe	Peripheral Component Interconnect Express
PF	Parity Flag
PGA	Pin Grid Array
PIC	Programmable Interrupt Controller
PID	Process ID
PIO	Programmed Input-Output
PMIO	Port-mapped Input-Output
PPID	Parent Process ID
PROM	Programmable Read Only Memory
RAID	Redundant Array of Independent Discs
RAM	Random Access Memory
RAW	Read After Write
RISC	Reduced Instruction Set Computer
ROM	Read Only Memory
rpm	revolutions per minute
RR	Round-Robin
SCSI	Small Computer System Interface
SDRAM	Synchronous Dynamic RAM
SI	Source Index (register)
SIMD	Single Instruction Multiple Data
SISD	Single Instruction Single Data
SJF	Shortest Job First
SMT	Simultaneous MultiThreading
SP	Stack Pointer
SR	Status Register
SRAM	Static Random Access Memory
SS	Stack Segment (register)
SSD	Solid State Drive
SSE	Streaming SIMD Extensions
TB	terabyte
TLB	Translation Lookaside Buffer
TLP	Task Level Parallelism
TTL	Transistor-Transistor Logic
UEFI	Unified Extensible Firmware Interface
UMA	Uniform Memory Access
USB	Universal Serial Bus
UTF	Unicode Transformation Format
VGA	Video Graphics Array
VLIW	Very Long Instruction Word

VM	Virtual Memory
VMM	Virtual Machine Manager
WAR	Write After Read
WAW	Write After Write
WB	Write Back
ZB	Zettabyte
ZBR	Zone Bit Recording
ZF	Zero Flag